IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Accompanying Application under 37 CFR 1.53:

Prior Application: K. HARA et al

Serial No. 08/184,070 Filed: January 21, 1994

Group Art Unit:

2824

Examiner:

A. Zarabian

For:

SEMICONDUCTOR MEMORY WITH ALTERNATIVELY MULTIPLEXED ROW AND COLUMN ADDRESSING

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Mail Stop Patent Application P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, the Applicants submit the documents cited on the attached PTO-1449 form.

These documents constitute the art of record in the parent application.

The Applicants request the Examiner initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

Registration No. 34,663 Attorney of Record

MATTINGLY, STANGER & MALUR, P.C. 1800 Diagonal Road, Suite 370 Alexandria, Virginia 22314 (703) 684-1120

Date: October 8, 2003

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ASA-312-02		SERIAL NO.			
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(Use several sheets if nece			sheets if necessa	ny) 	FILING DATE October 8, 2003	GROU	JP 		
				U.S. PATENT	DOCUMENTS				_
* EXAMINER INITIAL		DOCUMENT	DATE		NAME	CLASS	SUBCLASS	FILING (If Appn	
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	АВ	4,669,064	05/1987	Ishimoto					
	AC	4,750,839	06/1988	Wang et al				.41.	
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	AL	1-94592	04/13/89	Japanese Laid	-Open Patent				
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			OTHER DOCU	MENTS (Including Au	uthor, Title, Date, Pertinent I	Pages, etc.)			
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